

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	7	(true adj2 table) same gate same input same output	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 06:49
S2	1456	(truth adj2 table) and boolean	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 06:50
S3	2417	boolean same (logic or gate or element or circuit) and library	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 06:52
S4	206	S2 and S3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 06:52
S5	1726	(replac\$6 or substitut\$4) same (boolean or (truth adj2 table))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 06:53
S7	58	S4 and S5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 07:01
S8	30	716/1-5, 17-18.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 06:56
S9	0	S7 and S8	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 06:56
S10	38	S7 and (reduc\$6 near10 (circuit or logic or gate))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 07:08
S11	39	S7 and ((reduc\$6 or simplif\$4) near10 (circuit or logic or gate))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 07:08
S12	20	("5519627" "5521836" "5586047" "5659484" "5812417" "5838583" "5841663" "5850349" "6026222" "6056784" "6086629" "6145117" "6163876" "6286128" "6430734" "6434734" "6438736" "6584605" "6606737" "6687892").PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:35
S13	1273356	sequen\$5 or (hold\$3 near3 state) or dff or flip\$6	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:37
S14	2620	generat\$4 same (circuit or logic or gate) same boolean	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:37
S15	477	(substitut\$4 or replac\$6) same (circuit or logic or gate) same boolean	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:38

S16	682	equivalent same (circuit or logic or gate) same boolean	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:39
S17	57539	(circuit or cell or logic) same library	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:39
S18	652688	(reduc\$4 or simplif\$5) same (circuit or logic or gate)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:39
S19	2029	S13 and S14	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:40
S20	129	S15 and S16	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:40
S21	6079	S17 and S18	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:40
S22	44	S19 and S20	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:40
S23	10	S21 and S22	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:40
S24	1	S23 and truth adj2 table	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:42
S25	1	S24 and input and output	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:45
S27	1	S25 and (equivalen\$5 and simplification)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/27 08:54
S28	1409	('AND' adj2 gate) with ('MOS' or 'CMOS')	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/29 11:30
S29	4	('AND' adj2 gate) with ('MOS' or 'CMOS') same (conventionall or (widely adj us\$4) or well-known)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/29 11:32
S30	60	('AND' adj2 gate) with ('MOS' or 'CMOS') same (conventional\$4 or (widely adj us\$4) or well-known)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/29 15:00
S31	1	('AND' adj2 gate) with ('MOS' or 'CMOS') same (conventional\$4 or (widely adj us\$4) or well-known) and boolean	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/29 15:01